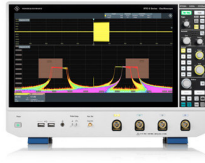


OSCILLOSCOPE PORTFOLIO



	R&S®RTH1000	R&S®RTC1000	R&S®RTB2000	R&S®RTM3000
Vertical system				
Bandwidth ¹⁾	60/100/200/350/500 MHz	50/70/100/200/300 MHz	70/100/200/300 MHz	100/200/350/500 MHz/1 GHz
Number of channels	2 plus DMM/4	2	2/4	2/4
ADC resolution; system architecture	10 bit; 16 bit	8 bit; 16 bit	10 bit; 16 bit	10 bit; 16 bit
V/div, 1 MΩ	2 mV to 100 V	1 mV to 10 V	1 mV to 5 V	500 μV to 10 V
V/div, 50 Ω	–			500 μV to 1 V
Horizontal system				
Sampling rate per channel (in Gsample/s)	1.25 (4-channel model); 2.5 (2-channel model); 5 (all channels interleaved)	1; 2 (2 channels interleaved)	1.25; 2.5 (2 channels interleaved)	2.5; 5 (2 channels interleaved)
Maximum memory (per channel; 1 channel active)	125 kpoints (4-channel model); 250 kpoints (2-channel model); 500 kpoints	1 Mpoints; 2 Mpoints	10 Mpoints; 20 Mpoints	40 Mpoints; 80 Mpoints
Segmented memory	standard, 50 Mpoints	–	option, 320 Mpoints	option, 400 Mpoints
Acquisition rate (in waveforms/s)	50 000	10 000	50 000 (300 000 in fast seg- mented memory mode ²⁾)	64 000 (2 000 000 in fast segmented memory mode ²⁾)
Trigger				
Types	digital	analog	analog	analog
Sensitivity	–	–	at 1 mV/div: > 2 div	at 1 mV/div: > 2 div
Mixed signal option (MSO)				
Number of digital channels ¹⁾	8	8	16	16
Analysis				
Mask test	tolerance mask	tolerance mask	tolerance mask	tolerance mask
Mathematics	elementary	elementary	basic (math on math)	basic (math on math)
Serial protocols triggering and decoding ¹⁾	I ² C, SPI, UART/RS-232/RS-422/ RS-485, CAN, LIN, CAN FD, SENT	I ² C, SPI, UART/RS-232/RS-422/ RS-485, CAN, LIN	I ² C, SPI, UART/RS-232/RS-422/ RS-485, CAN, LIN	I ² C, SPI, UART/RS-232/RS-422/RS-485, CAN, LIN, I ² S, MIL-STD-1553, ARINC429
Applications ^{1), 2)}	high-resolution frequency counter, advanced spectrum analysis, harmonics analysis, user scripting	digital voltmeter (DVM), com- ponent tester, fast Fourier trans- form (FFT)	digital voltmeter (DVM), fast Fourier transform (FFT), frequency response analysis	power, digital voltmeter (DVM), spectrum analysis and spectrogram, frequency response analysis
Compliance testing ^{1), 2)}	–	–	–	–
Display and operation				
Size and resolution	7" touchscreen, 800 × 480 pixel	6.5", 640 × 480 pixel	10.1" touchscreen, 1280 × 800 pixel	10.1" touchscreen, 1280 × 800 pixel
General data				
Dimensions in mm (W × H × D)	201 × 293 × 74	285 × 175 × 140	390 × 220 × 152	390 × 220 × 152
Weight in kg	2.4	1.7	2.5	3.3
Battery	lithium-ion, > 4 h	–	–	–

¹⁾ Upgradeable.²⁾ Requires an option.



R&S®MXO 4	R&S®MXO 5	R&S®RT06	R&S®RTP
200/350/500 MHz/1/1.5 GHz	100/200/350/500 MHz/1 GHz/2 GHz	600 MHz/1/2/3/4/6 GHz	4/6/8/13/16 GHz
4	4/8	4	4
12 bit; 18 bit	12 bit; 18 bit	8 bit; 16 bit	8 bit; 16 bit
500 µV to 10 V	500 µV to 10 V	1 mV to 10 V (HD mode: 500 µV to 10 V)	2 mV to 1 V (HD mode: 1 mV to 1 V)
500 µV to 1 V	500 µV to 1 V	1 mV to 1 V (HD mode: 500 µV to 1 V)	2 mV to 1 V (HD mode: 1 mV to 1 V)
2.5; 5 (2 channels interleaved)	5 on 4 channels; 2.5 on 8 channels (2 channels interleaved)	10; 20 (2 channels interleaved in 4 GHz and 6 GHz model)	20; 40 (2 channels interleaved)
standard: 400 Mpoints; max. upgrade: 800 Mpoints ²⁾	standard: 500 Mpoints max. upgrade: 1 Gpoints ²⁾	standard: 200 Mpoints/800 Mpoints; max. upgrade: 1 Gpoints/2 Gpoints	standard: 100 Mpoints/400 Mpoints; max. upgrade: 3 Gpoints
standard: 10 000 segments; option: 1 000 000 segments	standard: 10 000 segments; option: 1 000 000 segments	standard	standard
> 4 500 000	> 4 500 000 on 4 channels	1 000 000 (2 500 000 in ultra-segmented memory mode)	750 000 (3 200 000 in ultra-segmented memory mode)
digital	digital	digital (includes zone trigger)	advanced (includes zone trigger), digital trigger (14 trigger types) with real-time deembedding ²⁾ , high speed serial pattern trigger including 8/16 Gbps clock data recovery (CDR) ²⁾
0.0001 div, across full bandwidth, user controllable	0.0001 div, across full bandwidth, user controllable	0.0001 div, across full bandwidth, user controllable	0.0001 div, across full bandwidth, user controllable
16	16	16	16
basic (math on math)	basic (math on math)	user configurable, hardware based	user configurable, hardware based
I ² C, SPI, UART/RS-232/RS-422/RS-485, CAN, CAN FD, CAN XL, LIN	I ² C, SPI, UART/RS-232/RS-422/RS-485, CAN, CAN FD, CAN XL, LIN	advanced (formula editor, Python interface) I ² C, SPI, UART/RS-232/RS-422/RS-485, CAN, LIN, I ² S, MIL-STD-1553, ARINC 429, FlexRay™, CAN FD, MIPI RFFE, USB 2.0/HSIC, MDIO, 8b10b, Ethernet, Manchester, NRZ, SENT, MIPI D-PHY, SpaceWire, MIPI M-PHY/UniPro, CXPI, USB 3.1 Gen 1, USB-SSIC, PCIe 1.1/2.0, USB Power Delivery, Automotive Ethernet 100/1000BASE-T1	advanced (formula editor, Python interface) I ² C, SPI, UART/RS-232/RS-422/RS-485, SENT, CAN, LIN, CAN FD, MIL-STD-1553, ARINC 429, SpaceWire, USB 2.0/HSIC/PD, USB 3.1 Gen 1/Gen 2/SSIC, PCIe 1.1/2.0/3.0, 8b10b, MIPI RFFE, MIPI D/M-PHY/UniPro, Automotive Ethernet 100/1000BASE-T1, Ethernet 10/100BASE-TX, MDIO, Manchester, NRZ
power, digital voltmeter (DVM), frequency response analysis	power, frequency response analysis	power, advanced spectrum analysis and spectrogram, jitter and noise decomposition, clock data recovery (CDR), I/Q data and RF analysis (R&S®VSE), deembedding, TDR/TDT analysis	advanced spectrum analysis and spectrogram, jitter and noise decomposition, real-time deembedding, TDR/TDT analysis, I/Q data and RF analysis (R&S®VSE), advanced eye diagram
–	–	see data sheet (PD 5216.1640.22)	see data sheet (PD 3683.5616.22)
13.3" touchscreen, 1920 × 1080 pixel (Full HD)	15.6" touchscreen, 1920 × 1080 pixel (Full HD)	15.6" touchscreen, 1920 × 1080 pixel (Full HD)	13.3" touchscreen, 1920 × 1080 pixel (Full HD)
414 × 279 × 162	445 × 314 × 154	450 × 315 × 204	441 × 285 × 316
6	9	10.7	18
–	–	–	–