



SP1000G series datasheet and user manual

1GSPS logic analyzer and pattern generator

www.ikalogic.com | support@ikalogic.com

April 4, 2022

Contents

SP1000G Series overview	4
Typical applications	4
Product highlights	5
Warning	6
SP1000G system architecture	7
Main characteristics	8
Operating conditions	8
Timing and measurements	8
Active probes set specifications (input mode)	8
Power requirements	9
SP1000G Interfaces	9
SP1018G host device interfaces	10
Active probe set interfaces	11
Principle of operation	11
Embedded memory vs streaming	11
Clocking architecture	12
Timebase	12
State mode	13
Continuous output clock	13
Versatile trigger system	13
External trigger OUT specifications	14
External trigger IN specifications	15
Active probes	16
Equivalent schematic of 1 channel (standard active probe)	16
What's in the box	16
Unpacking and first usage	17
Host status LEDs behavior	17
Active probe LED behavior	17
Software Quick Start guide	18
Capturing your first signal	18

